| Notice of Allowability | Application No. | Applicant(s) | | |
|--|--|---|----------------------------|--|
| | 10/605,010 | HU ET AL. | | |
| | Examiner | Art Unit | | |
| | Tu-Tu Ho | 2818 | | |
| The MAILING DATE of this communication appeal All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R | (OR REMAINS) CLOSED in this a or other appropriate communicati IGHTS. This application is subjec | application. If not includion will be mailed in due | led course. THIS | |
| 1. This communication is responsive to <u>Paper filed 08/23/200</u> | <u>04</u> . | | | |
| 2. ⊠ The allowed claim(s) is/are <u>1-13</u> . | | | I, | |
| 3. $igotimes$ The drawings filed on <u>01 September 2003</u> are accepted by | / the Examiner. | | | |
| 4. Acknowledgment is made of a claim for foreign priority ur a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority do | e been received. e been received in Application No. | · · · · · · · · · · · · · · · · · · · | ation from the | |
| International Bureau (PCT Rule 17.2(a)). * Certified copies not received: | | | | |
| Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. | of this communication to file a rep MENT of this application. | oly complying with the re | quirements | |
| A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give | | | NOTICE OF | |
| 6. CORRECTED DRAWINGS (as "replacement sheets") mus | st be submitted. | | • | |
| (a) ☐ including changes required by the Notice of Draftspers | son's Patent Drawing Review (PT | O-948) attached | | |
| 1) 🗌 hereto or 2) 📗 to Paper No./Mail Date | | | | |
| (b) including changes required by the attached Examiner' Paper No./Mail Date | s Amendment / Comment or in the | e Office action of | • | |
| Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t | | | e back) of | |
| DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT | | | Note the | |
| Attachment(s) 1. ☑ Notice of References Cited (PTO-892) | 5. ☐ Notice of Informa | al Patent Application (PT | ·O-152) | |
| Notice of Draftperson's Patent Drawing Review (PTO-948) | 6. ☐ Interview Summa | • | 0 102) | |
| 3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/0 | | Paper No./Mail Date 7. | | |
| Paper No./Mail Date | , | ment of Reasons for All | 014/2000 | |
| of Biological Material | 9. ☐ Other | inent of Neasons for All | owance | |
| | | | | |
| David Nelms Supervisory Patent Examiner Technology Center 2800 | | | | |

DETAILED ACTION

Election/Restrictions

1. Claim 1 of Species I is generic and allowable. Accordingly, the restriction requirement between the two species is hereby withdrawn.

In view of the above noted withdrawal of the restriction requirement as to the linked species, applicant(s) are advised that if any claim(s) depending from or including all the limitations of the allowable generic linking claim(s) be presented in a continuation or divisional application, such claims may be subject to provisional statutory and/or nonstatutory double patenting rejections over the claims of the instant application. Once a restriction requirement is withdrawn, the provisions of 35 U.S.C. 121 are no longer applicable. See *In re Ziegler*, 44 F.2d 1211, 1215, 170 USPQ 129, 131-32 (CCPA 1971). See also MPEP § 804.01.

Allowable Subject Matter

2. Claims 1-13 are allowable over the prior art of record.

The following is an examiner's statement of reasons for allowance: The prior art of record fails to teach or render obvious a CMOS device having all exclusive limitations as recited in claims 1 and 10, comprising at least two thin-film transistors (TFT) of a first conductivity type and a second conductivity type, each TFT having a polysilicon island including a pair of source/drain regions (S/D) sandwiching a channel region, an inter-layer dielectric layer (ILD) having a plurality of contacts and covering the at least two TFTs, a conductive line formed on the ILD, and a plurality of S/D contact metals disposed on and through the ILD, the pair of S/D of one TFT of the at least two TFTs being arranged along the same direction as the pair of S/D of the other TFT or the at least two TFTs being arranged parallel to each other, and the conductive line being oriented in a direction perpendicular to the direction of the pair of S/D (the limitation

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"the conductive line being oriented in a direction perpendicular to the direction of the pair of S/D" is the same as the conductive line being parallel to a gate line on either side of which is a source or drain) and electrically connecting a S/D of one TFT with a S/D of the other TFT.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

- The prior art made of record and not relied upon is considered pertinent to applicant's 3. disclosure. Copies of the references are not being furnished with this Office Action per MPEP § 707.05(a).
- U.S. Patent 6,001,714 to Nakajima et al. discloses an LCD driving circuit a) comprising a pair of CMOS TFTs where a channel length of one TFT is perpendicular to a channel length of the other TFT.
- U.S. Patent 6,590,228 to Voutsas et al. discloses an LCD device having a first set b) of TFTs and a second set of TFTs, the first set of TFTs having channel lengths perpendicular to those of the second set of TFTs, but all channel lengths are parallel to a predominant crystal orientation.

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4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tu-Tu Ho whose telephone number is (571) 272-1778. The examiner can normally be reached on 6:30 am - 5:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, DAVID NELMS can be reached on (571) 272-1787. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Tu-Tu Ho September 09, 2004 David Nelms
Supervisory Patent Examiner
Technology Center 2800

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